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FIG. 1
(PRIOR ART)

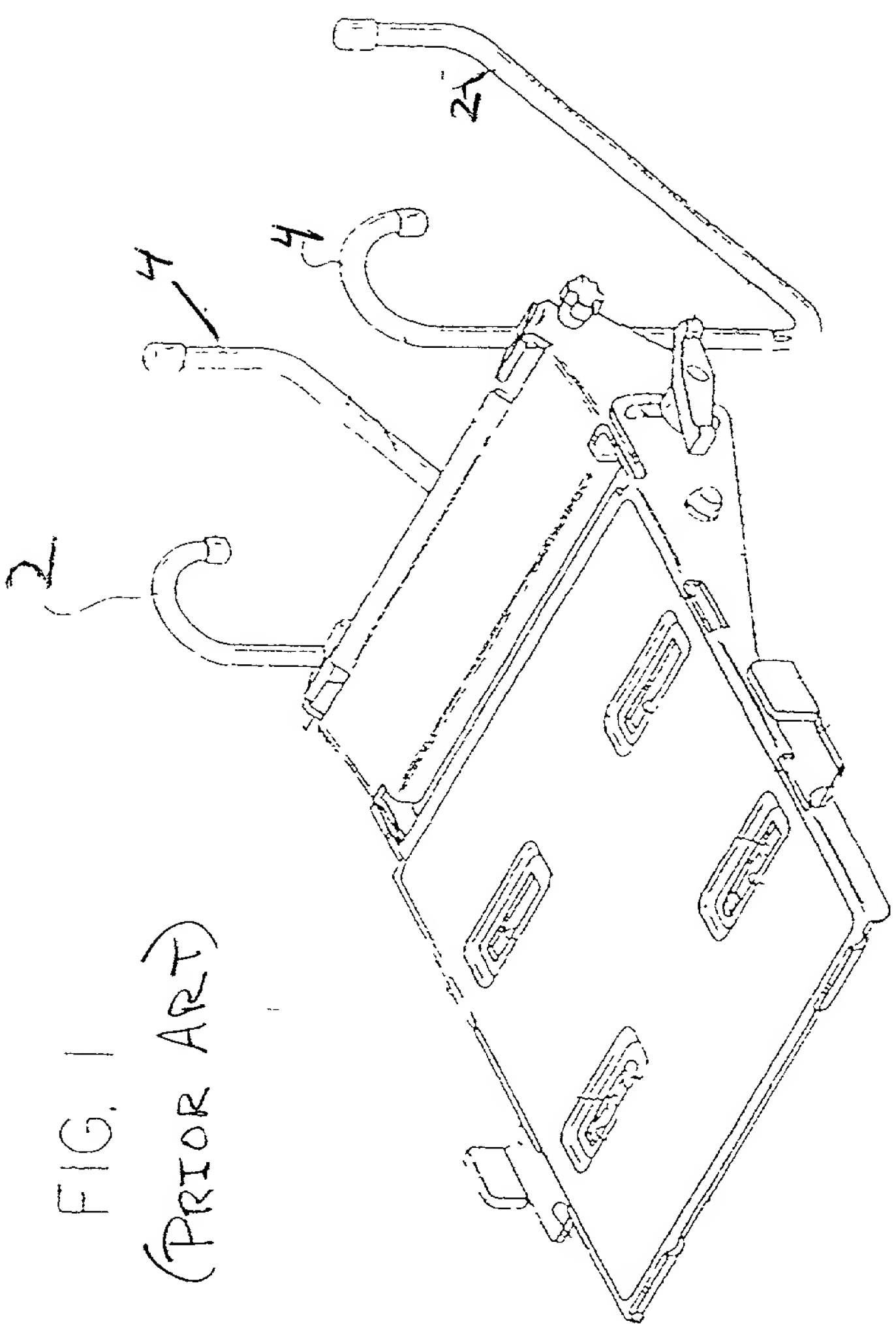


FIG. 2A and FIG. 2B are schematic diagrams of a prior art device.

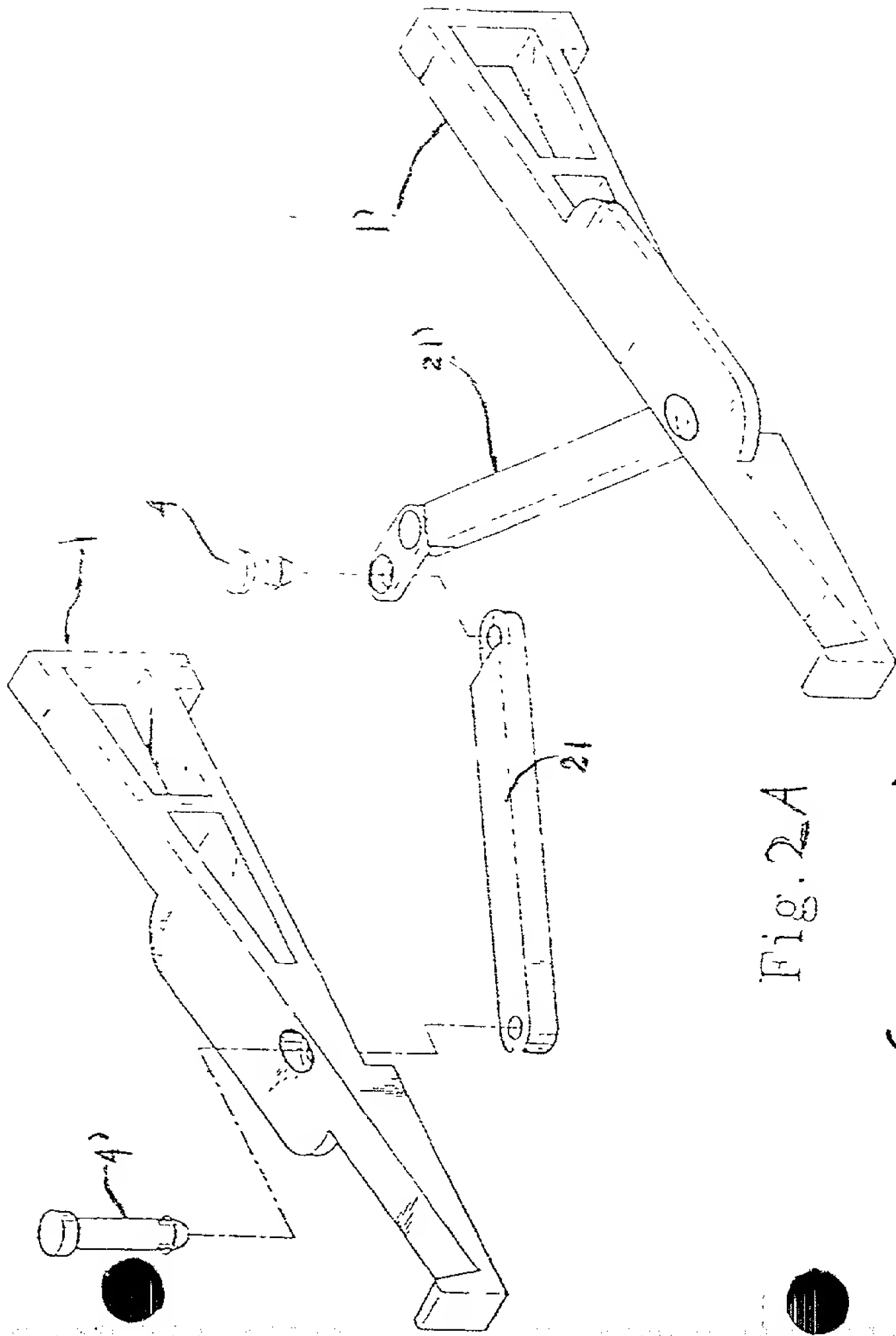


Fig. 2A
(PRIOR ART)

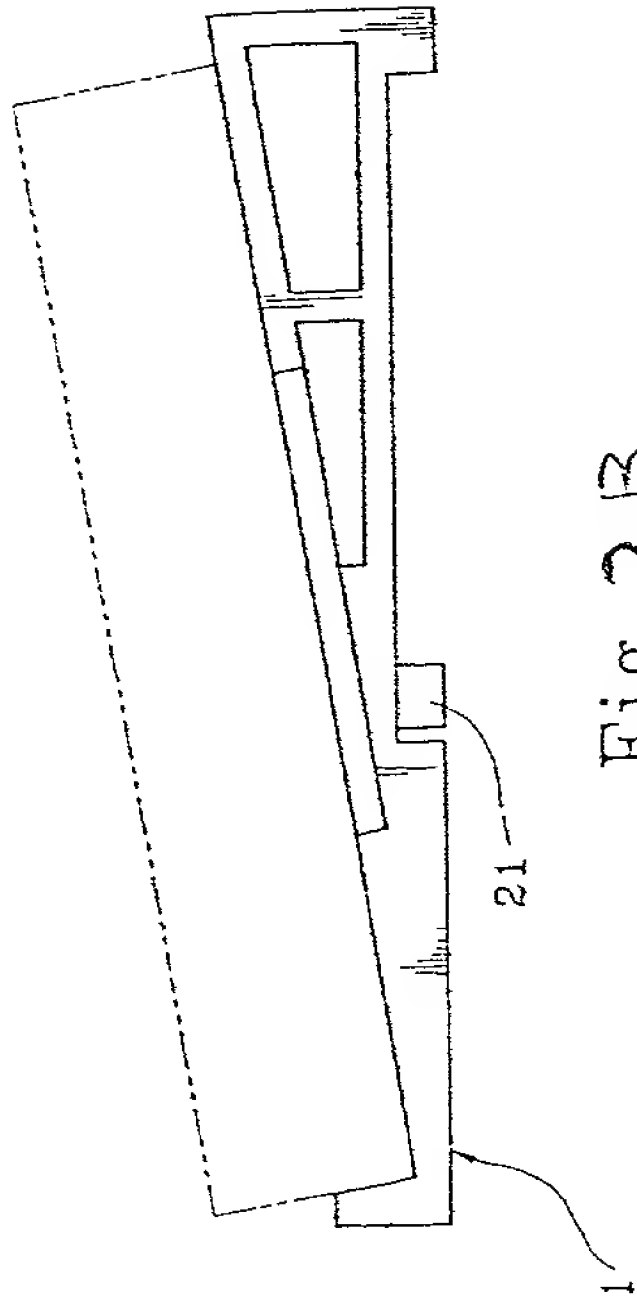
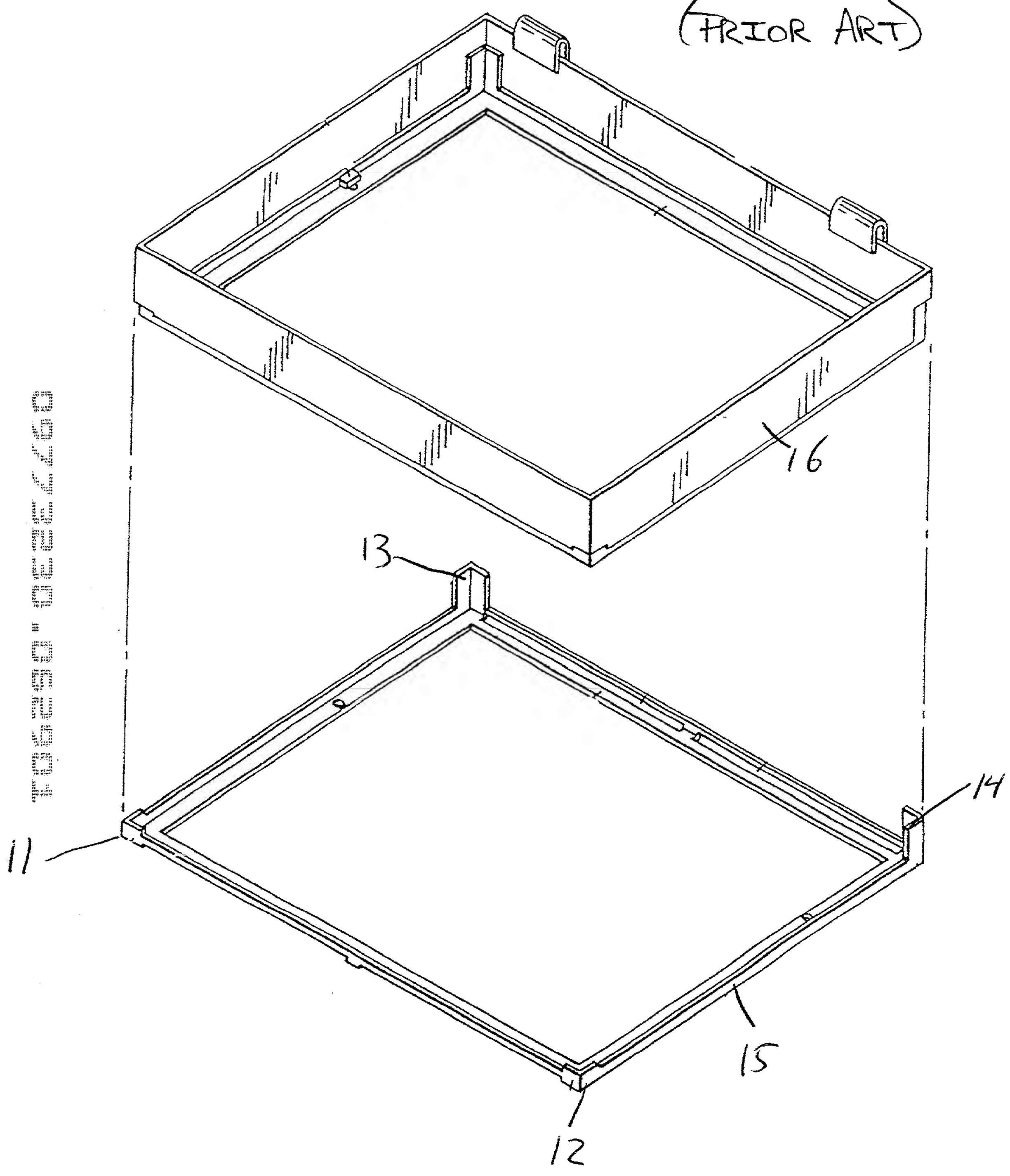


Fig. 2B
(PRIOR ART)

FIG. 3
(PRIOR ART)



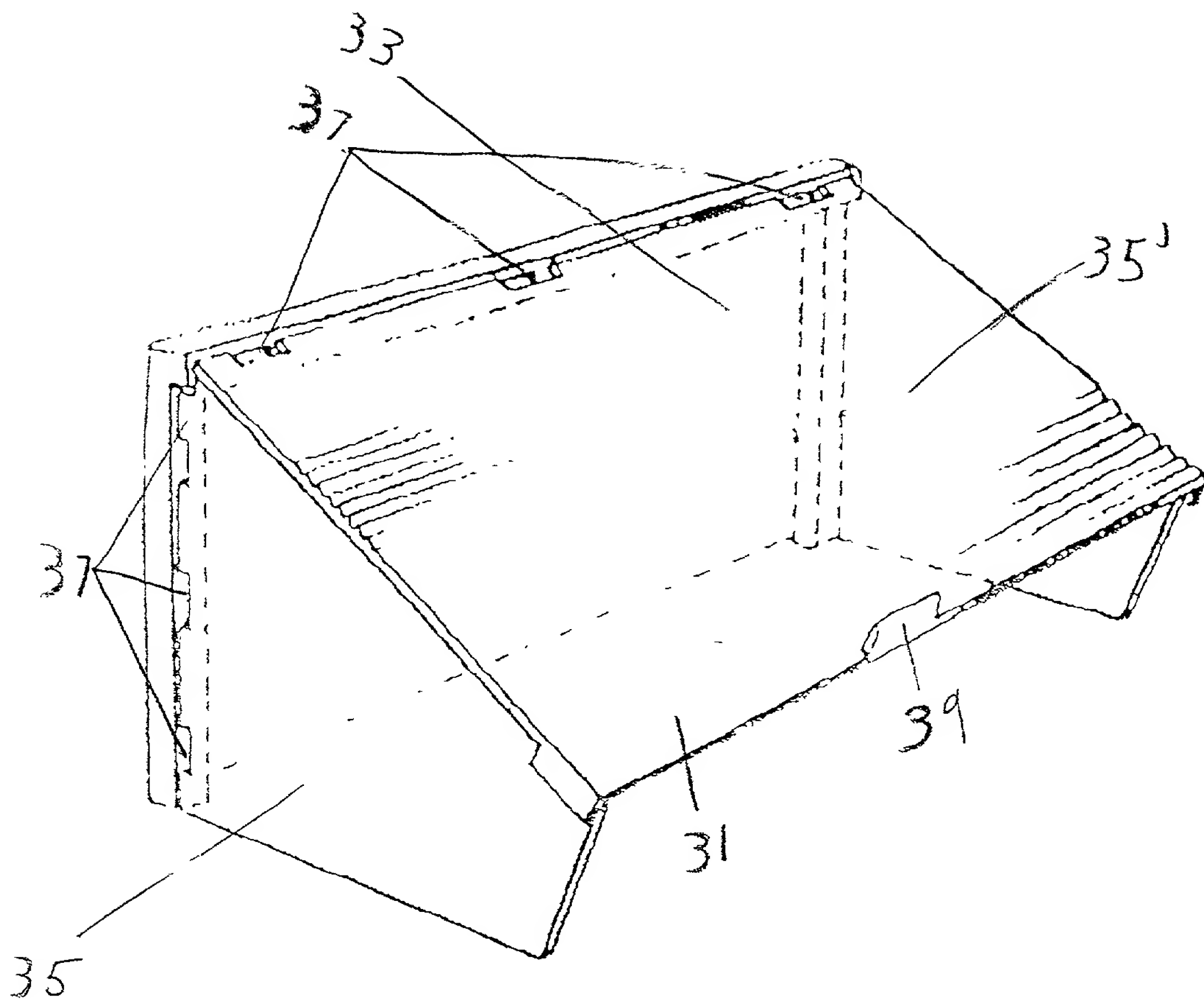


FIG. 4

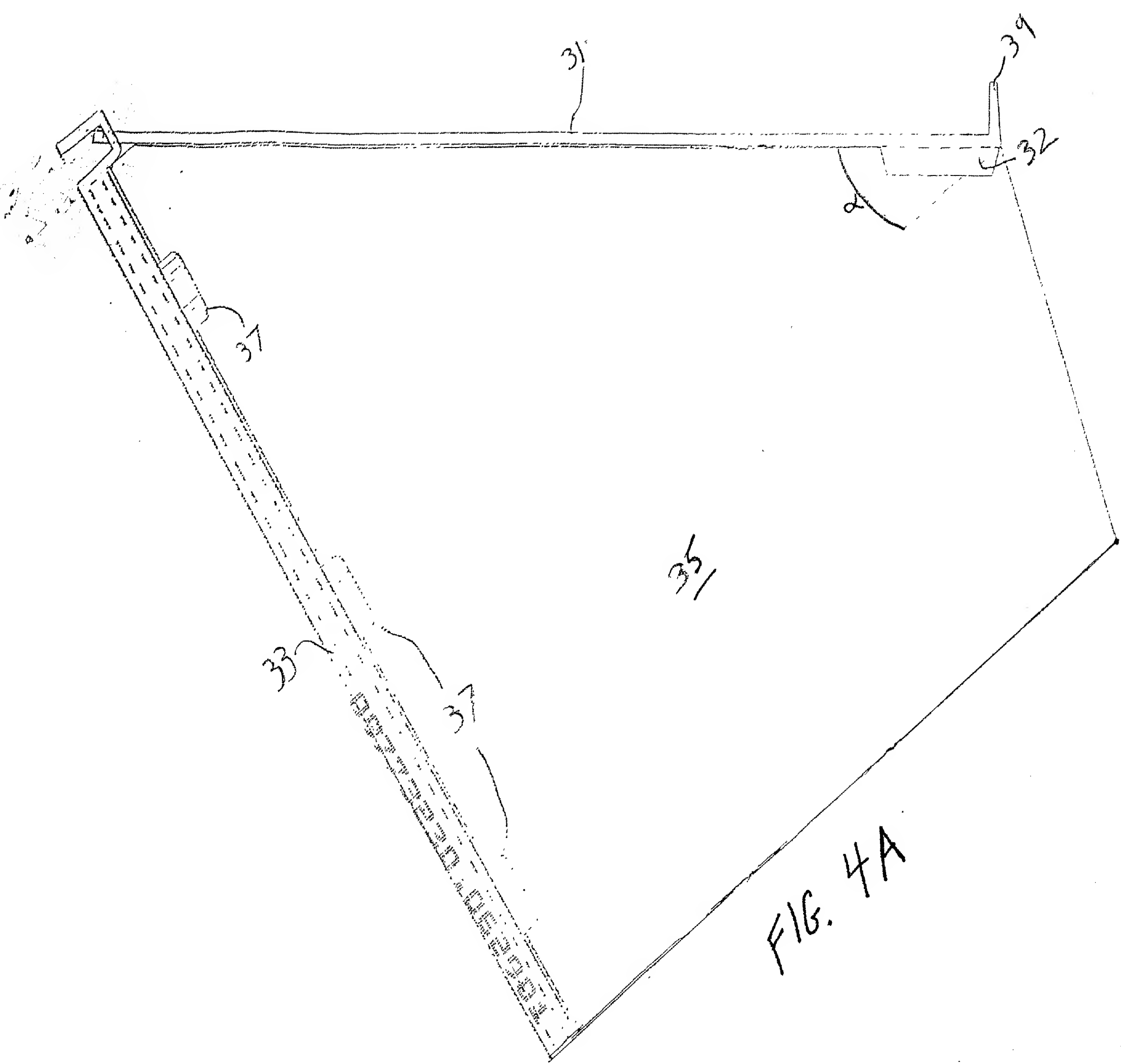
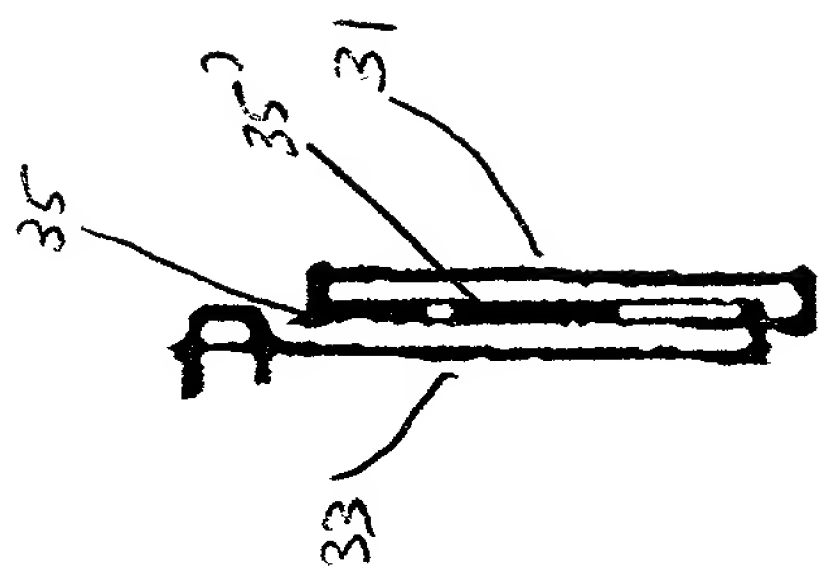
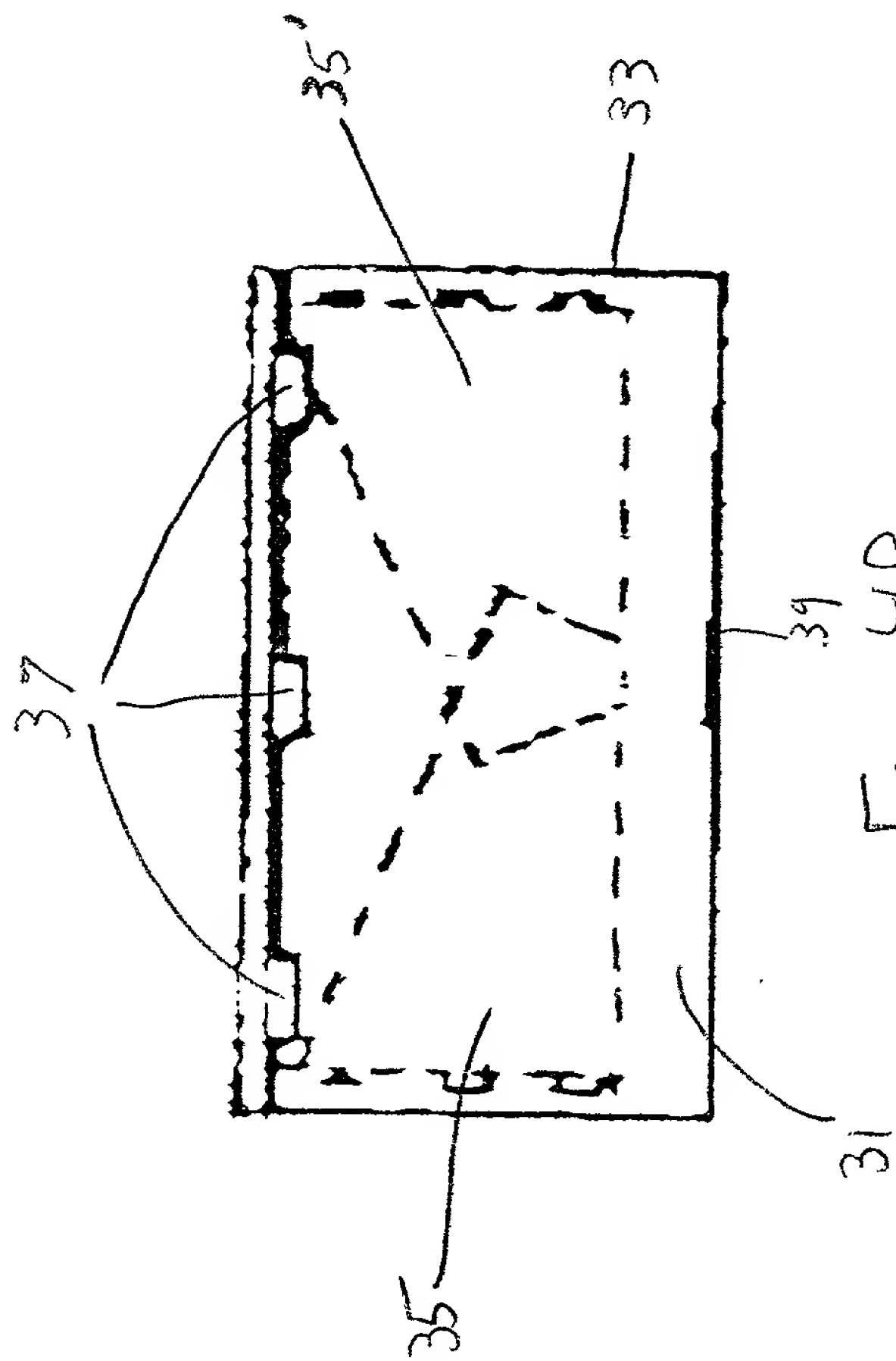


FIG. 4A



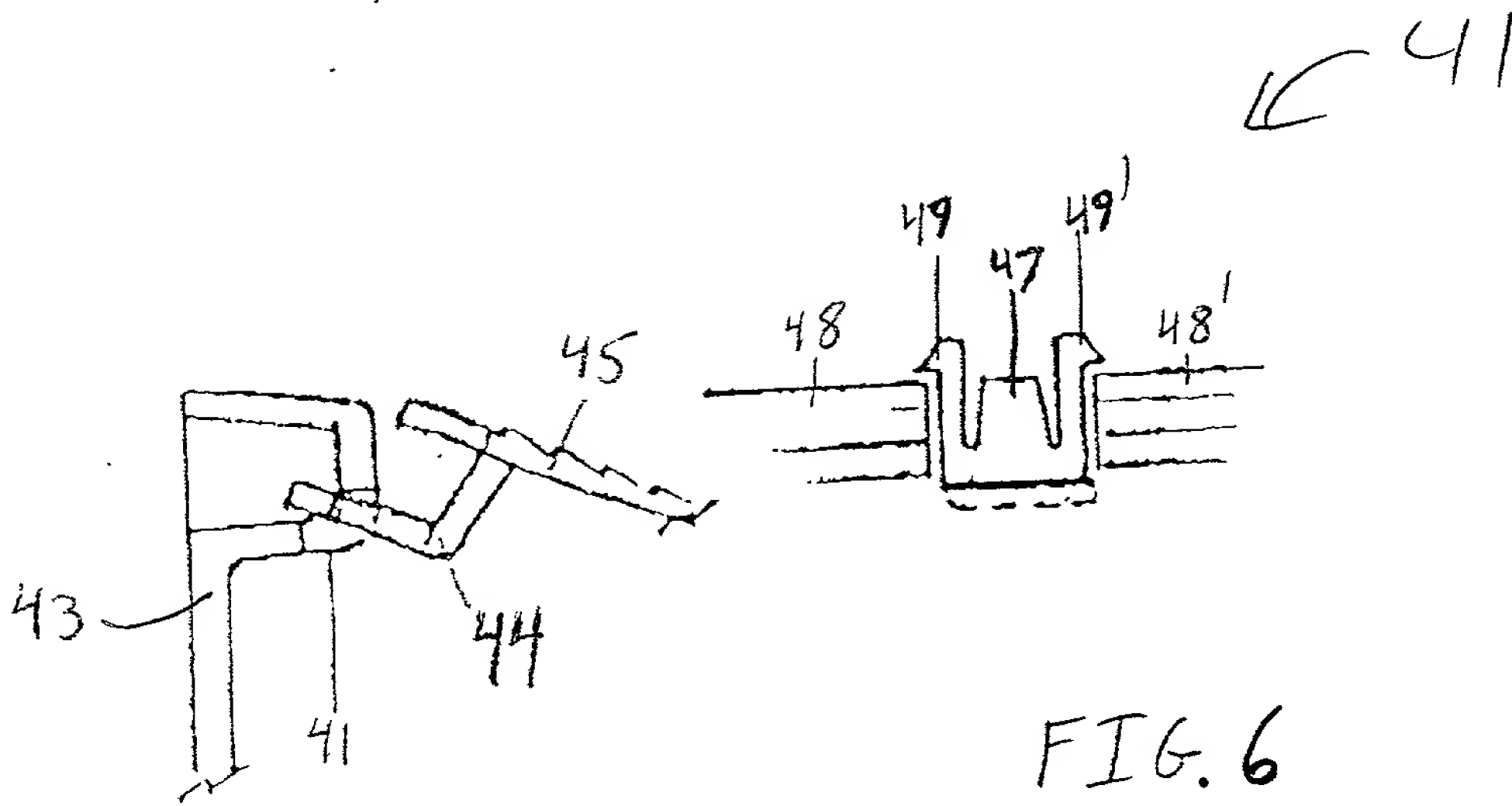


FIG. 6

FIG. 5

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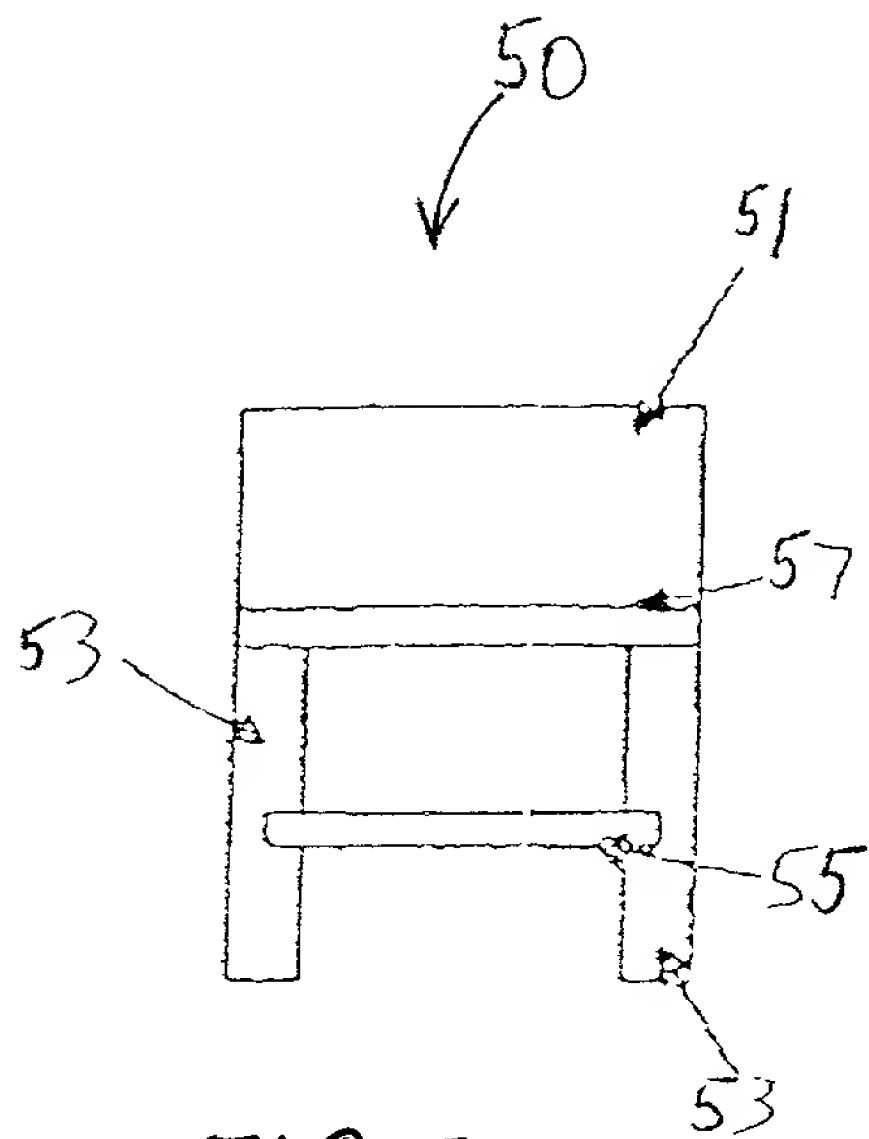


FIG. 7A

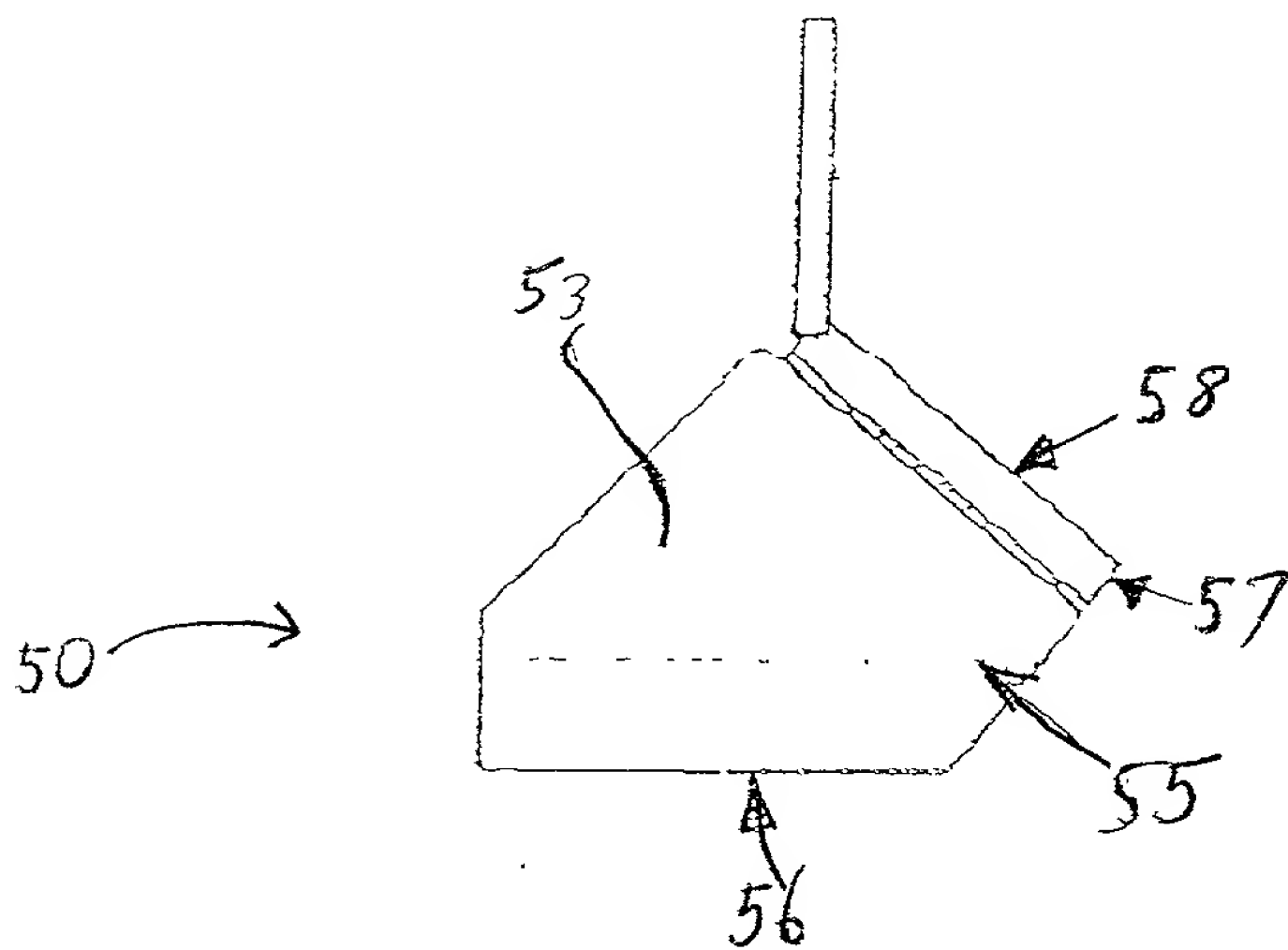


FIG. 7B

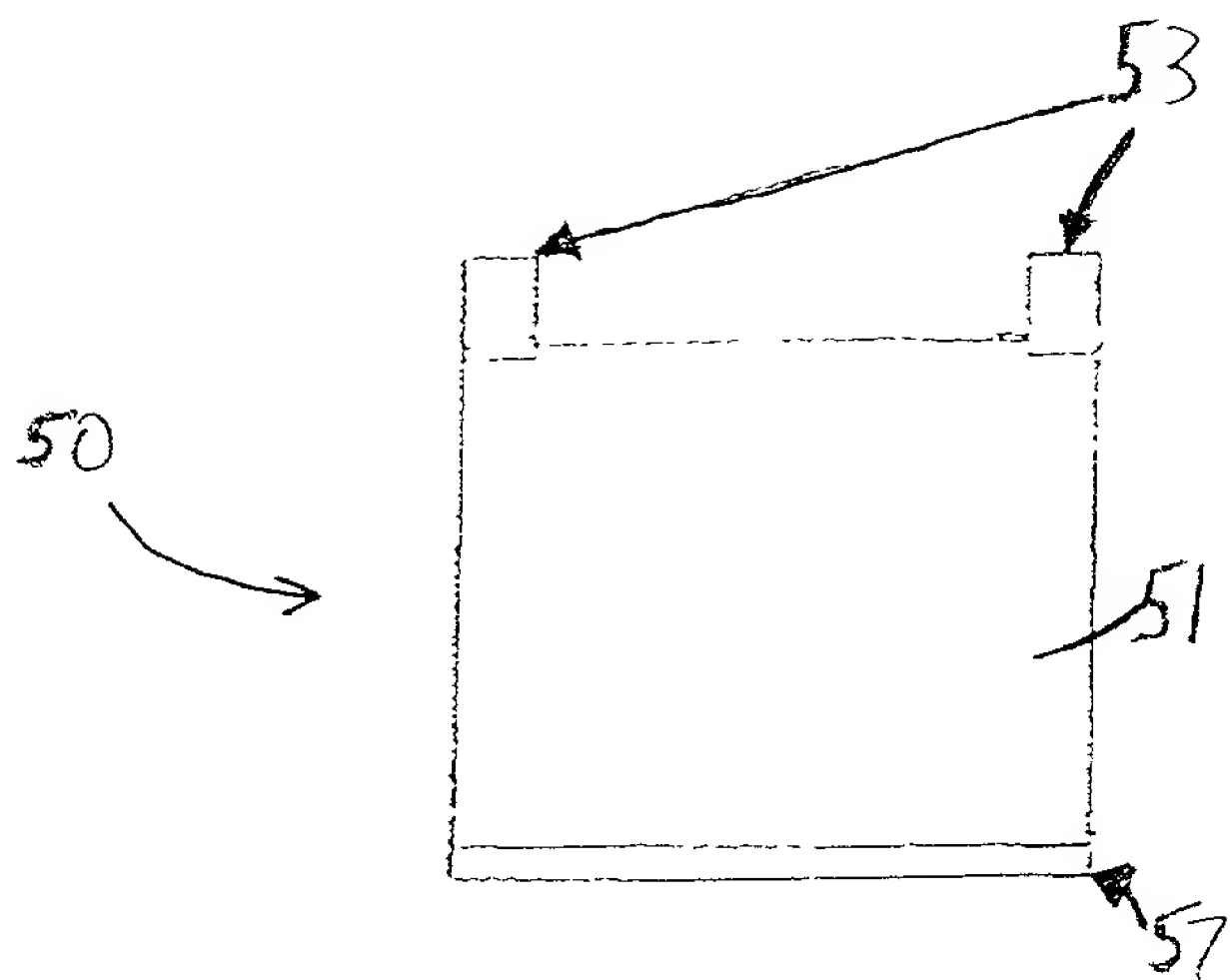


FIG. 7C